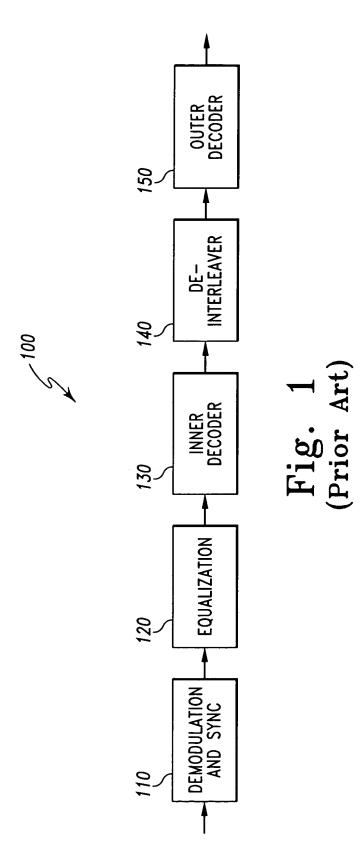
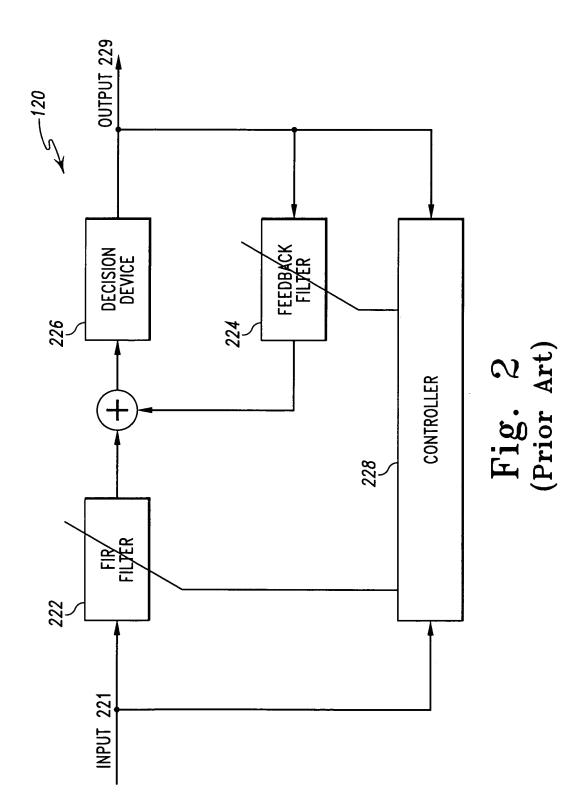
COMBINED TRELLIS DECODER AND DECISION FEEDBACK EQUALIZER Inventor: Jingsong Xia, et al. App. No. 09/884,256 Atty Docket No. 31075-7 Drawing sheet 1 of 15



COMBINED TRELLIS DECODER AND DECISION • FÉEDBACK EQUALIZER Inventor: Jingsong Xia, et al. App. No. 09/884,256 Atty Docket No. 31075-7 Drawing sheet 2 of 15

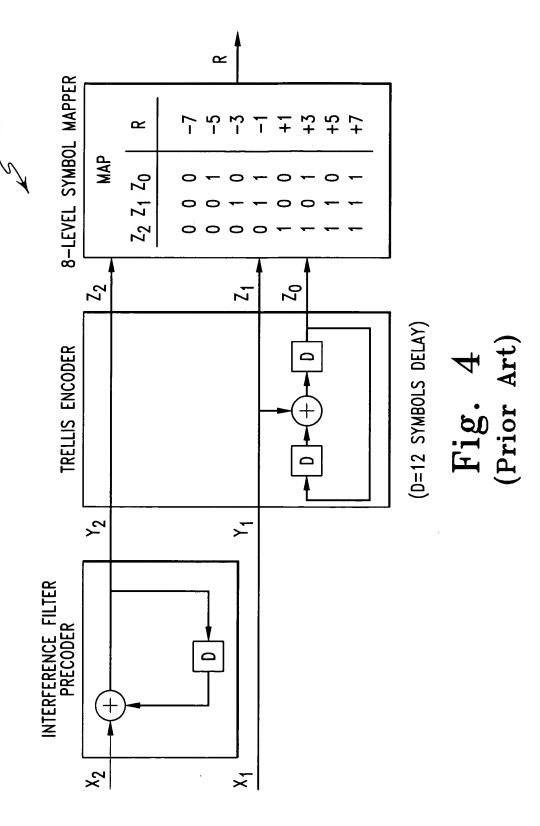


COMBINED TRELLIS DECODER AND DECISION FEEDBACK EQUALIZER Inventor: Jingsong Xia, et al. App. No. 09/884,256 Atty Docket No. 31075-7 Drawing sheet 3 of 15 NOV 0 6 2002 PRINT & TRANSP MTH DELAY DECISION FEEDBACK EQUALIZER 2ND DELAY **OUTPUT 229** 1ST DELAY Z⁻¹ INPUT DECISION DEVICE, SLICER TRELLIS DECODER ELAY 350 2ND DELAY 품 ERROR STEP SIZE 1ST DELAY 222 221 NEOT



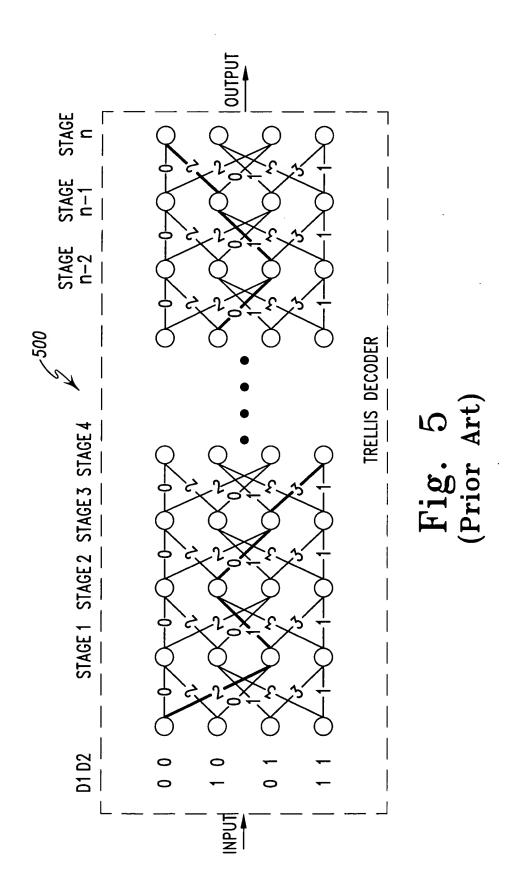
COMBINED TRELLIS DECODER AND DECISION FEEDBACK EQUALIZER Inventor: Jingsong Xia, et al. App. No. 09/884,256 Atty Docket No. 31075-7

Drawing sheet 4 of 15



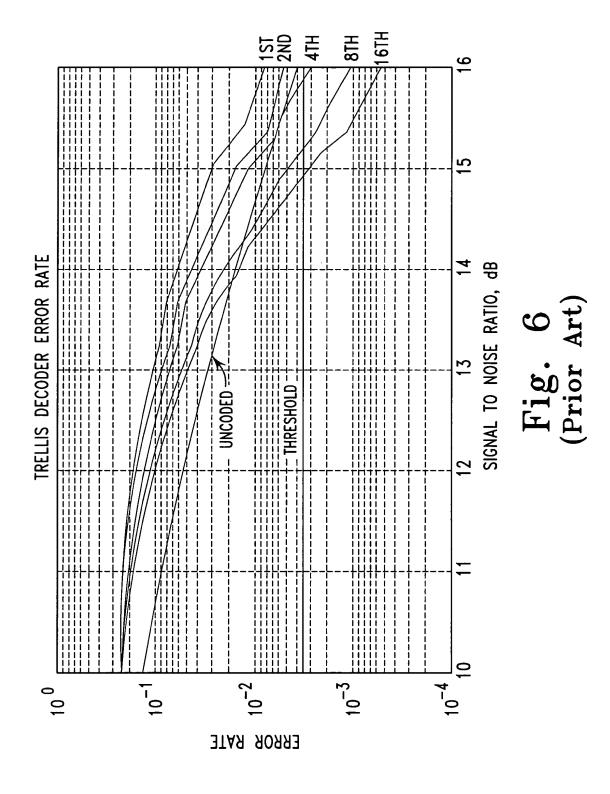
App. No. 0

COMBINED TRELLIS DECODER AND DECISION
FEEDBACK EQUALIZER
Inventor: Jingsong Xia, et al.
App. No. 09/884,256 Atty Docket No. 31075-7
Drawing sheet 5 of 15

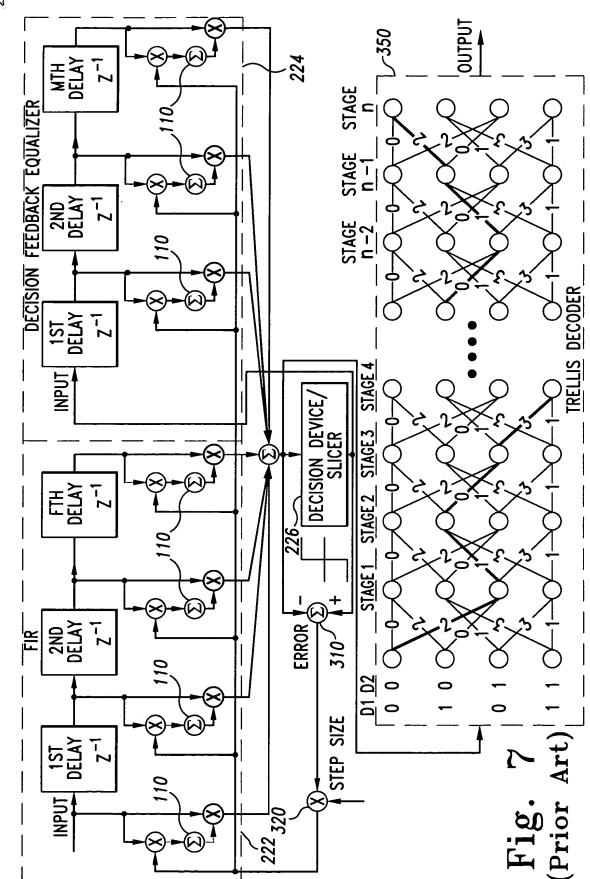


COMBINED TRELLIS DECODER AND DECISION FEEDBACK EQUALIZER Inventor: Jingsong Xia, et al. App. No. 09/884,256 Atty Docket No. 31075-7 Drawing sheet 6 of 15





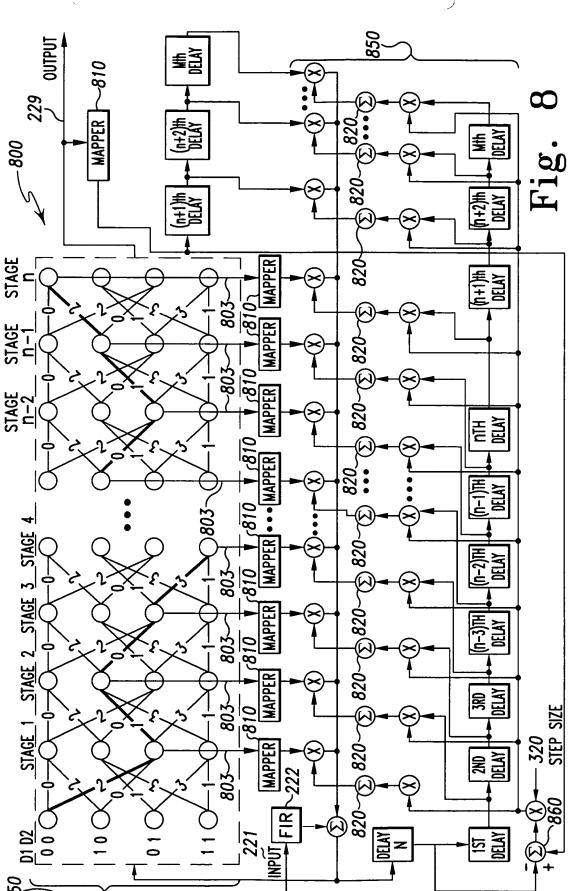
COMBINED TRELLIS DECODER AND DECISION
FEEDBACK EQUALIZER
Inventor: Jingsong Xia, et al.
App. No. 09/884,256 Atty Docket No. 31075-7
Drawing sheet 7 of 15



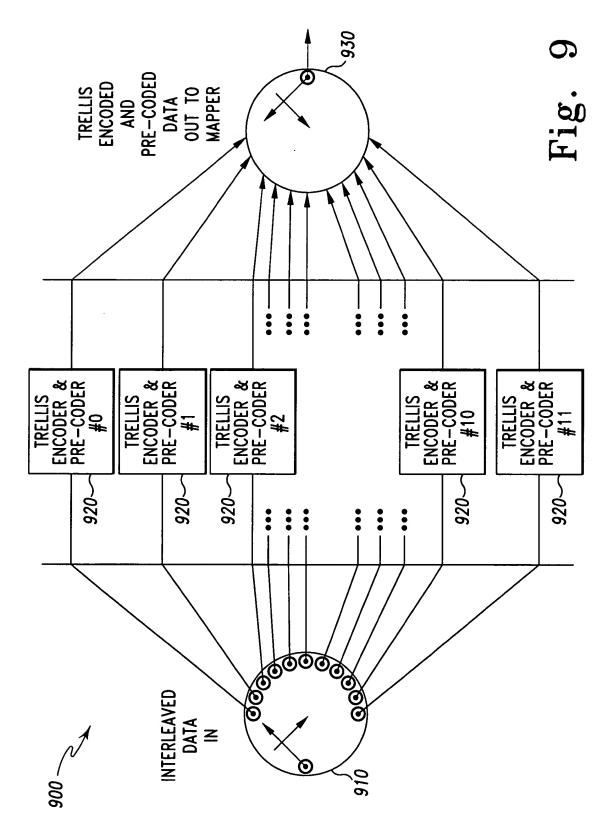
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COMBINED TRELLIS DECODER AND DECISION FEEDBACK EQUALIZER

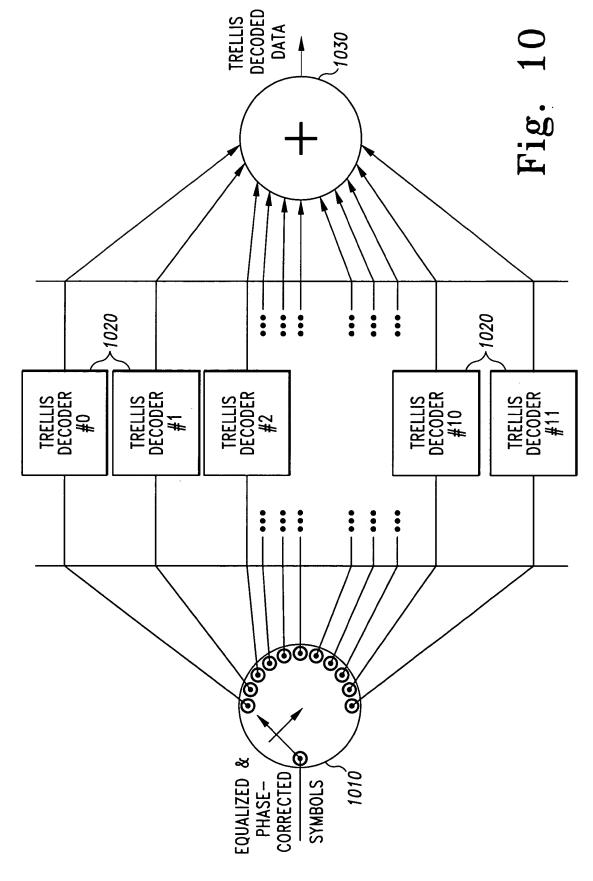
Inventor: Jingsong Xia, et al. App. No. 09/884,256 Atty Docket No. 31075-7 Drawing sheet 8 of 15

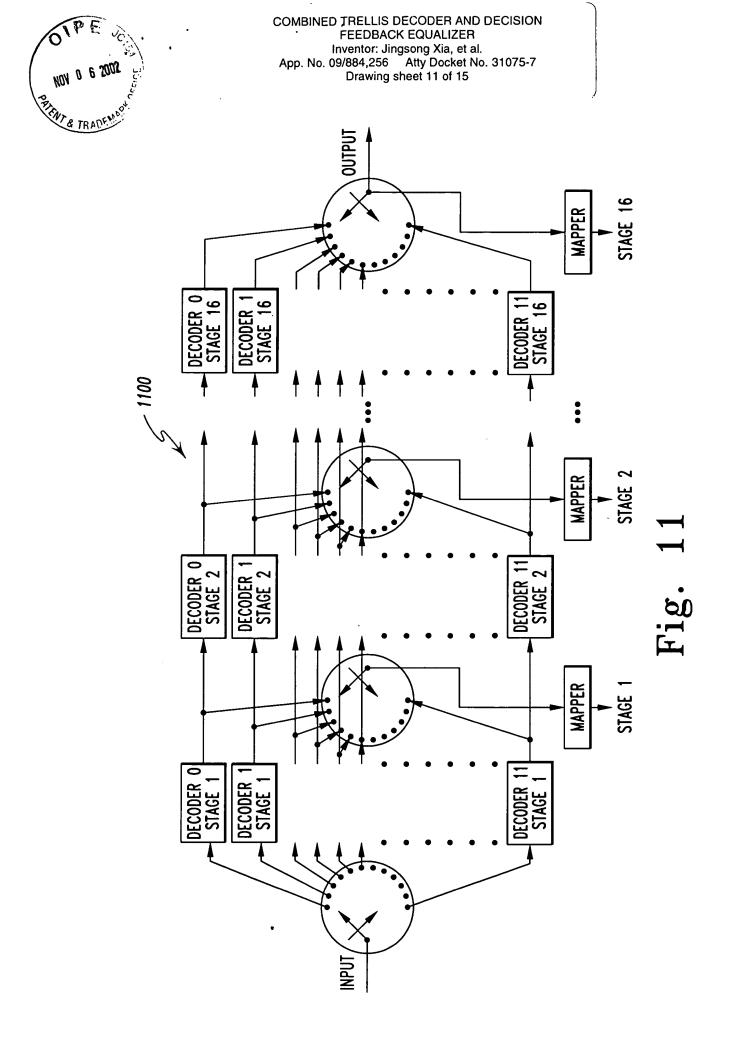


COMBINED TRELLIS DECODER AND DECISION FEEDBACK EQUALIZER Inventor: Jingsong Xia, et al. App. No. 09/884,256 Atty Docket No. 31075-7 Drawing sheet 9 of 15



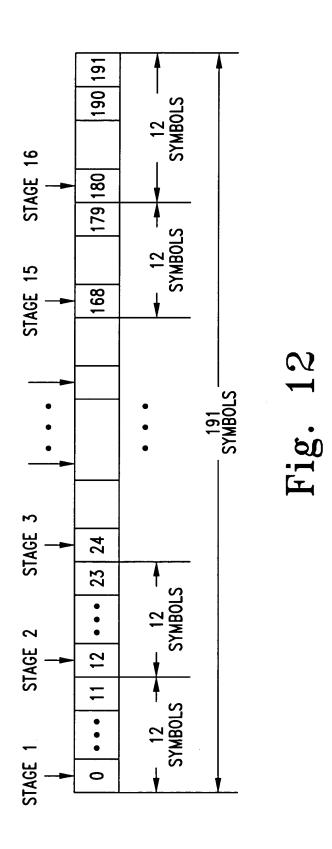
COMBINED TRELLIS DECODER AND DECISION FEEDBACK EQUALIZER Inventor: Jingsong Xia, et al. App. No. 09/884,256 Atty Docket No. 31075-7 Drawing sheet 10 of 15





COMBINED TRELLIS DECODER AND DECISION
. FEEDBACK EQUALIZER
Inventor: Jingsong Xia, et al.
App. No. 09/884,256 Atty Docket No. 31075-7
Drawing sheet 12 of 15





COMBINED TRELLIS DECODER AND DECISION FEEDBACK EQUALIZER 61Pc Inventor: Jingsong Xia, et al. 09/884,256 Atty Docket No. 31075-7 App. No. 09/884,256 Atty Docket Drawing sheet 13 of 15 NOV 0 6 2002 FRADE! Fig. 13 12TH DELAY 192TH DELAY BANK16 2ND DELAY 181ST DELAY 1ST DELAY 180TH DELAY 7-1 TRELLIS DECODER OUTPUT STAGE 16 ERROR, 1-7 12TH DELAY 12TH DELAY BANK STEP SIZE 2ND DELAY 2ND DELAY 1-1 320 1-7 1ST DELAY 1ST DELAY STAGE 1 DELAY C INPUT

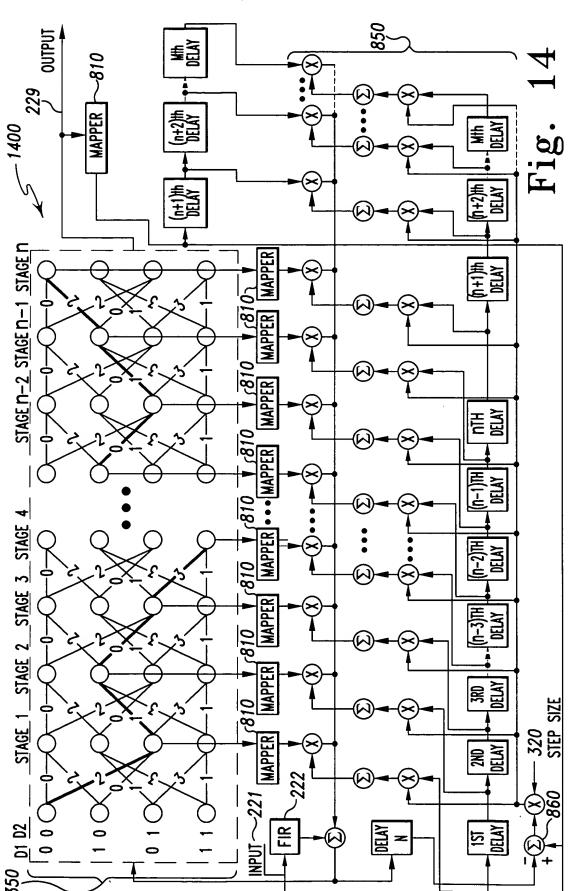
OIPE WOOD BOOK STRANGERS

COMBINED TRELLIS DECODER AND DECISION FEEDBACK EQUALIZER

Inventor: Jingsong Xia, et al.

App. No. 09/884,256 Atty Docket No. 31075-7

Drawing sheet 14 of 15



COMBINED TRELLIS DECODER AND DECISION
FEEDBACK EQUALIZER
Inventor: Jingsong Xia, et al.
App. No. 09/884,256 Atty Docket No. 31075-7
Drawing sheet 15 of 15 -150012TH DELAY 192TH DELAY Fig. 15 BANK16 2ND DELAY 181ST DELAY 1ST DELAY 180TH DELAY TRELLIS DECODER OUTPUT STAGE 16 ERROR 12TH DELAY Z^{-1} 12TH DELAY BANK STEP SIZE 2ND DELAY 2ND DELAY 320 1ST DELAY DECODED AND MAPPED DATA 1ST DELAY STAGE 1

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PARADENT RADENT